

Electronic Acknowledgement Receipt

EFS ID:	1086761
Application Number:	10016449
Confirmation Number:	5286
Title of Invention:	Method and system for use of a field programmable gate array (FPGA) function within an application specific integrated circuit (ASIC) to enable creation of a debugger client within the ASIC
First Named Inventor:	Robert Thomas Bailis
Customer Number:	47052
Filer:	Kelvin M. Vivian/Jinny Nguyen
Filer Authorized By:	Kelvin M. Vivian
Attorney Docket Number:	RPS920010127US1
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Application Type:	Utility
International Application Number:	

Payment information:

Submitted with Payment	no
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part	Pages
1		RPS920010127US1FinalAm end.pdf	1266624	yes	9

	Multipart Description		
	Doc Desc	Start	End
	Amendment After Final	1	1
	Claims	2	5
	Applicant Arguments/Remarks Made in an Amendment	6	9

Warnings:

Information:

2	Transmittal letter	RPS920010127US1Transm. pdf	244279	no	1
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Warnings:

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Total Files Size (in bytes):	1510903
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New Applications Under 35 U.S.C. 111

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.